FIG 4A

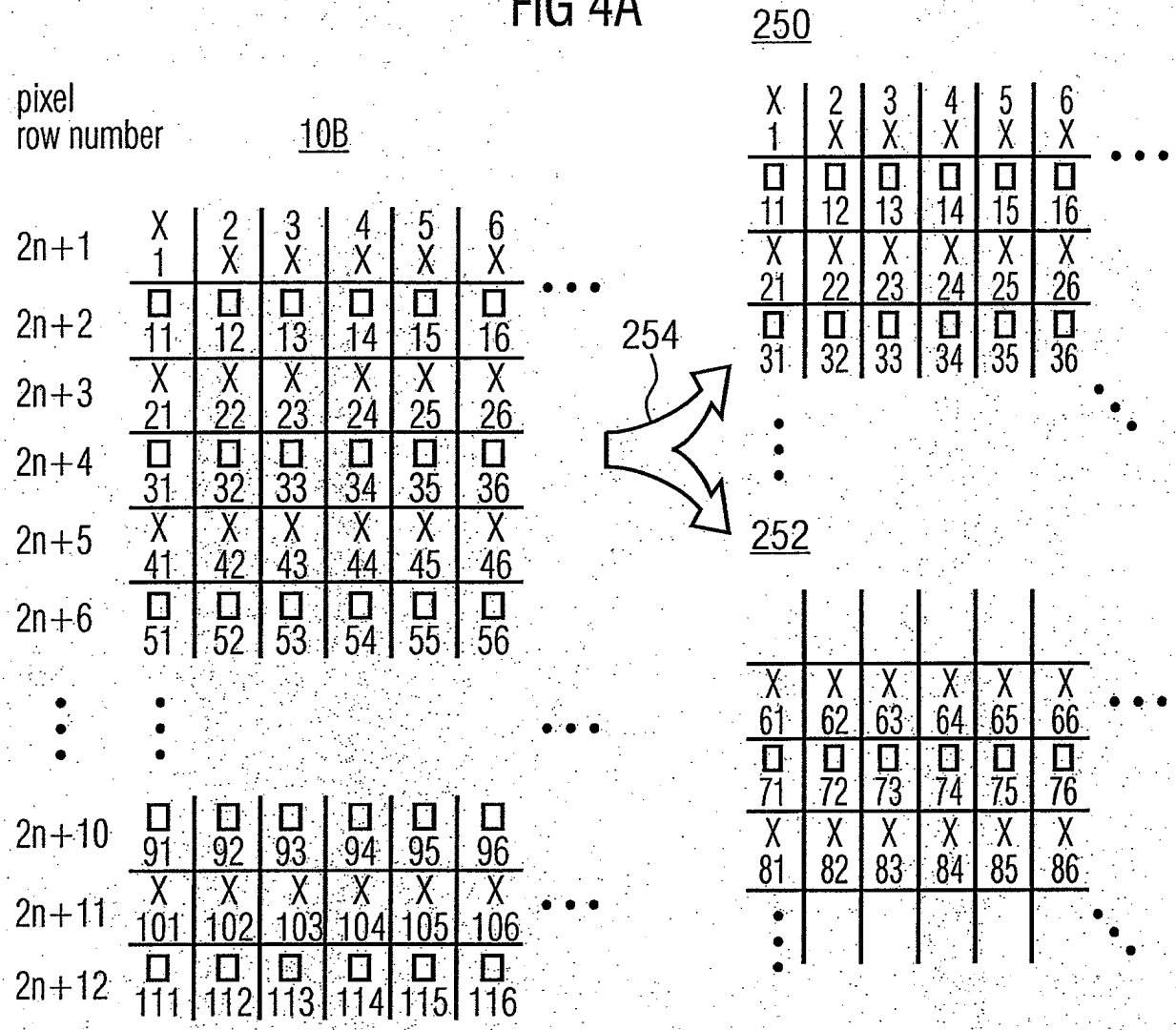

250
pixel
row number
10B
254
252
2n+1
2n+2
2n+3
2n+4
2n+5
2n+6
2n+10
2n+11
2n+12

FIG 2

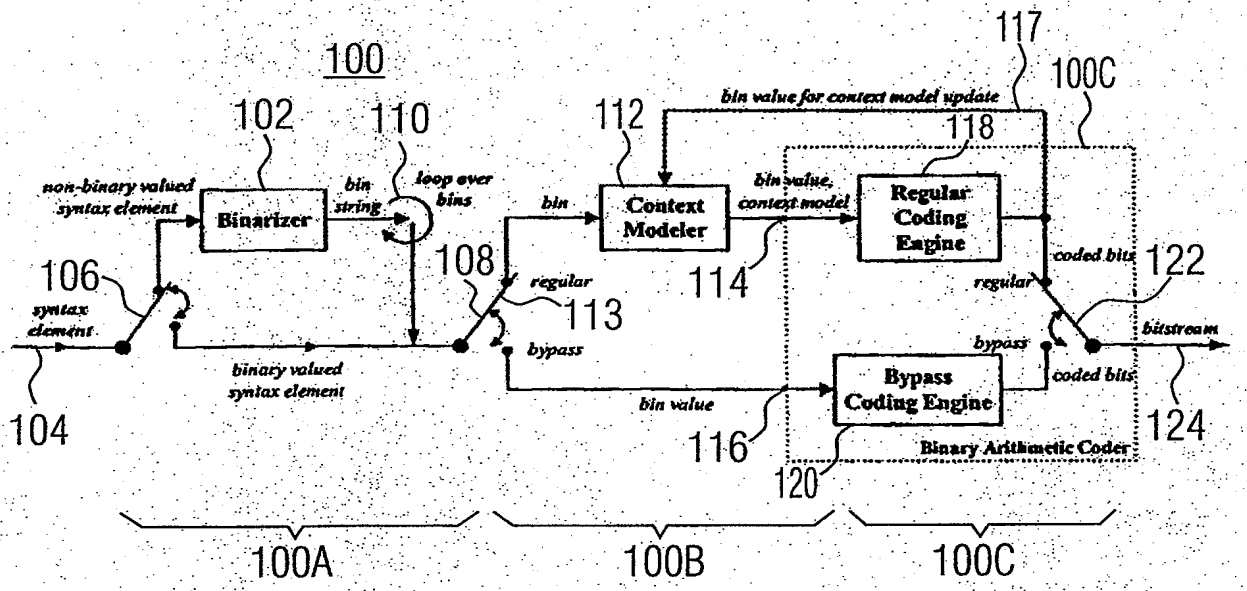

100
102
110
112
117
100C
118
bin value for context model update
non-binary valued syntax element
Binarizer
bin string
loop over bins
bin
Context Modeler
bin value, context model
Regular Coding Engine
coded bits
122
106
syntax element
104
108
regular
113
bypass
114
binary valued syntax element
bin value
Bypass Coding Engine
116
120
Binary Arithmetic Coder
bitstream
124
100A
100B
100C

FIG 4B

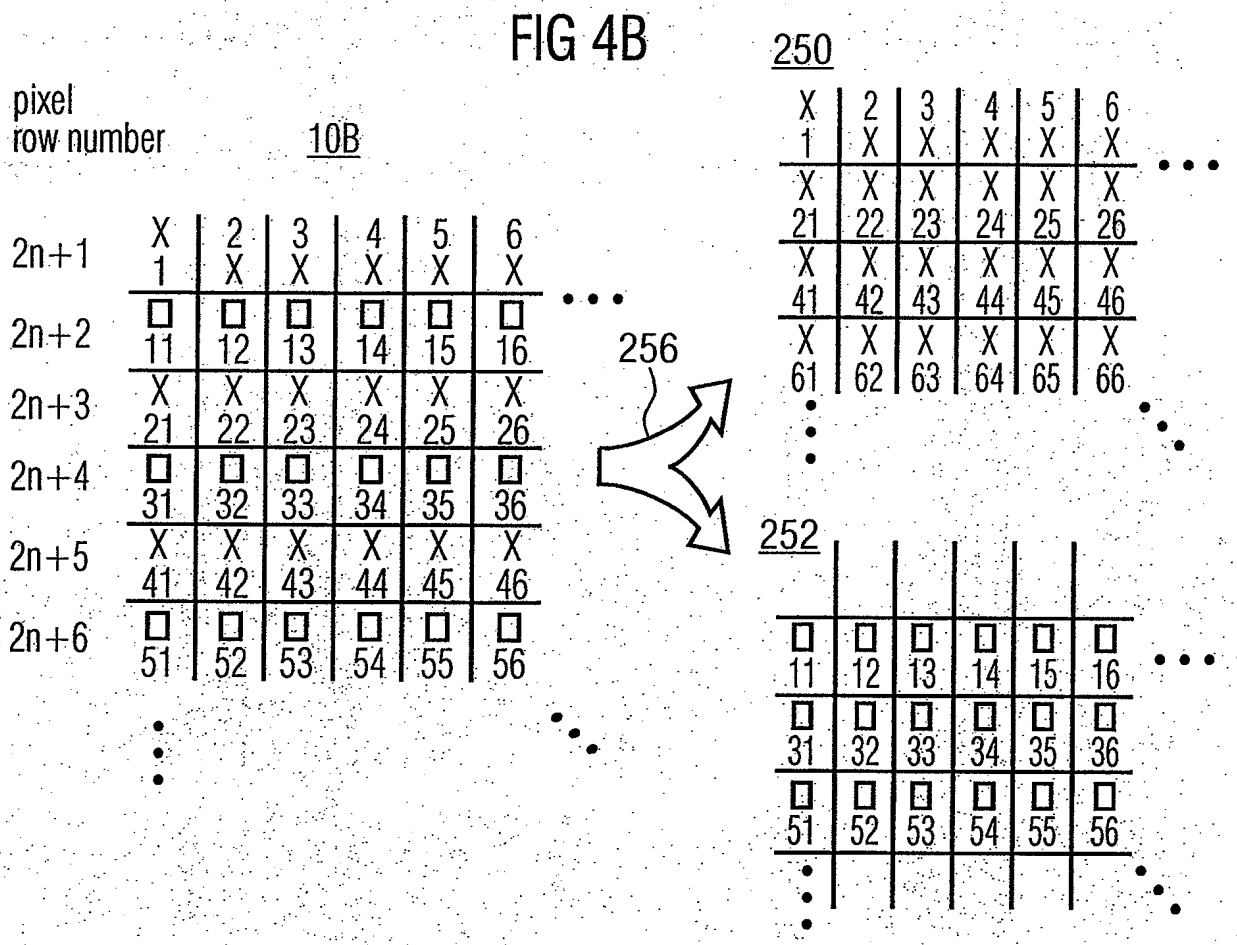


FIG 3

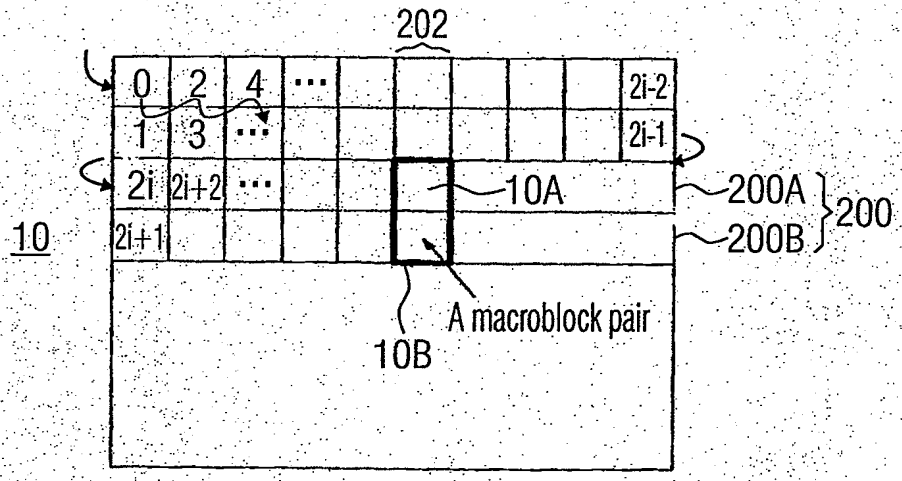


FIG 7

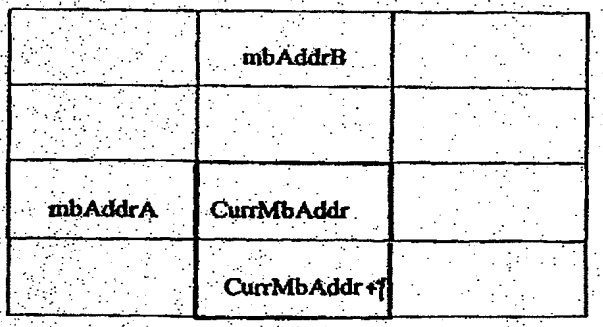
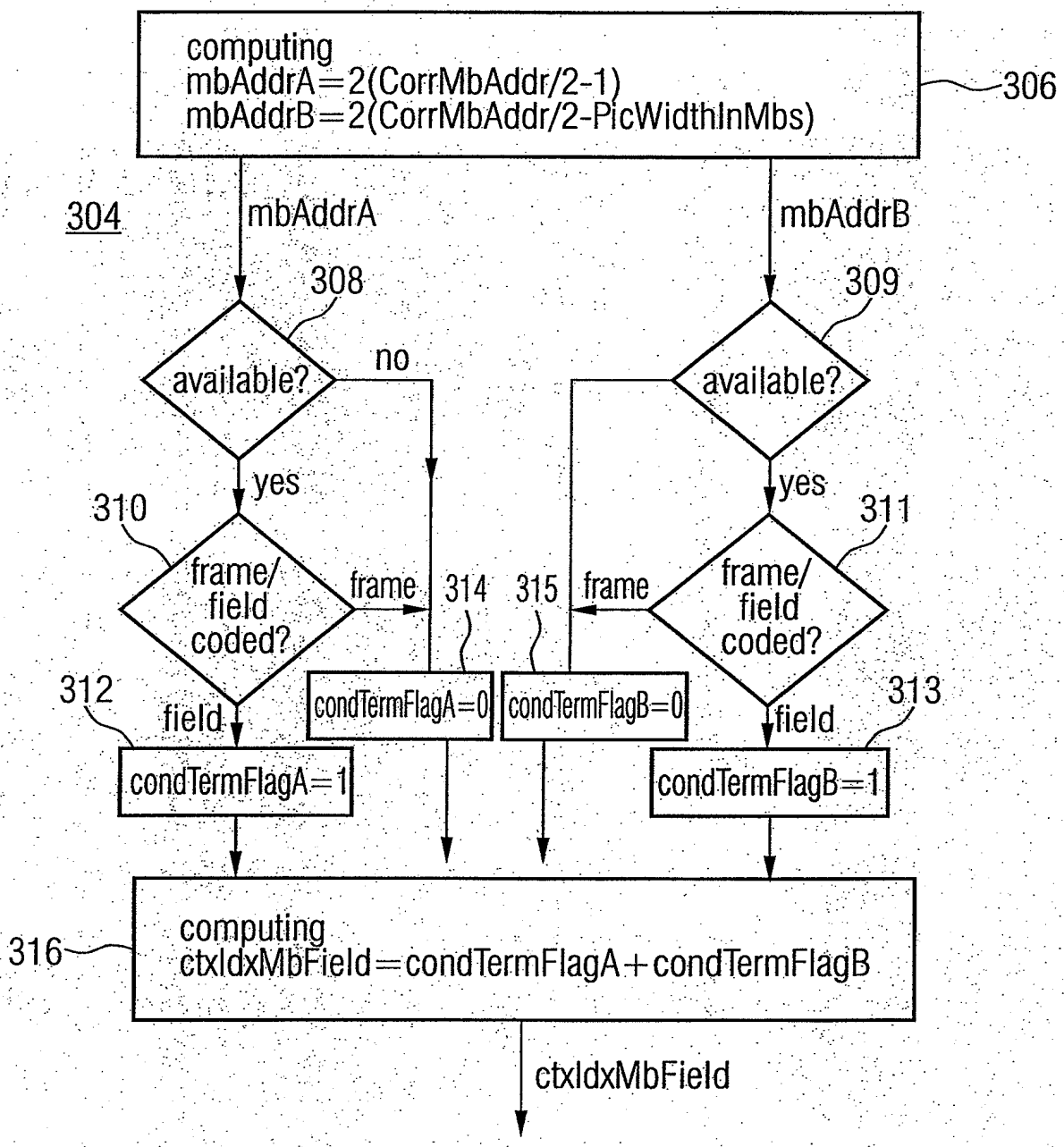


FIG 6


computing
mbAddrA=2(CorrMbAddr/2-1)
mbAddrB=2(CorrMbAddr/2-PicWidthInMbs)
306
304
mbAddrA
mbAddrB
308
309
available?
available?
no
yes
yes
310
311
frame/
field
coded?
frame/
field
coded?
frame
314
315
frame
312
313
field
field
condTermFlagA=0
condTermFlagB=0
condTermFlagA=1
condTermFlagB=1
316
computing
ctxIdxMbField=condTermFlagA+condTermFlagB
ctxIdxMbField